

Notice of References Cited	Application/Control No. 09/932,556	Applicant(s)/Patent Under Reexamination GERARD CHAUVEL	
	Examiner LeChi Truong	Art Unit 2126	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	C	US-4,814,974	03-1989	Narayanan et al.	710/244
	D	US-5,581,722	12-1996	Welland, Robert V.	711/207
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	L	US-			
	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

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	U	David Eck, xLogicCircuits Lab 2: Memory Circuits, 1997
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.